

1/2

FIG. 1

CS _i	CS _i --> CS _{i+1}	CS _i --> CS _{i-1}
CS1	AV_RXQUAL_LT < CS_QUAL_UL_1_2	Impossible
CS2	AV_RXQUAL_LT < CS_QUAL_UL_2_3	AV_RXQUAL_LT > CS_QUAL_UL_1_2 + CS_HST_UL_LT AV_RXQUAL_ST > CS_QUAL_UL_1_2 + CS_HST_UL_ST
CS3	AV_RXQUAL_LT < CS_QUAL_UL_3_4	AV_RXQUAL_LT > CS_QUAL_UL_2_3 + CS_HST_UL_LT AV_RXQUAL_ST > CS_QUAL_UL_2_3 + CS_HST_UL_ST
CS4	Impossible	AV_RXQUAL_LT > CS_QUAL_UL_3_4 + CS_HST_UL_LT AV_RXQUAL_ST > CS_QUAL_UL_3_4 + CS_HST_UL_ST

TABLE 1: STATE TRANSITIONS

2/2

FIG-2

CS _i	CS _i → CS _{i+1}	CS _i → CS _{i-1}
CS1	AV_RXQUAL_LT < CS_QUAL_DL_1_2	Impossible
CS2	AV_RXQUAL_LT < CS_QUAL_DL_2_3	AV_RXQUAL_LT > CS_QUAL_DL_1_2 + CS_HST_DL_LT AV_RXQUAL_ST > CS_QUAL_DL_1_2 + CS_HST_DL_ST
CS3	AV_RXQUAL_LT < CS_QUAL_DL_3_4 AV_SIR > CS_SIR_DL_3_4	AV_RXQUAL_LT > CS_QUAL_DL_2_3 + CS_HST_DL_LT AV_RXQUAL_ST > CS_QUAL_DL_2_3 + CS_HST_DL_ST
CS4	Impossible	AV_SIR < CS_SIR_DL_3_4 + CS_SIR_HST_DL